SPECIFICATIONS				
CUSTOMER :				
SAMPLE CODE (Ver.)				
MASS PRODUCTION CODE (Ver.)	PC2004LRU-CWA-HDHQ (Ver.0)			
DRAWING NO. (Ver.)	PC-95040 			

# **Customer Approved**

Date:

Approved	QC Confirmed	Designer
The same of the sa		马运办

- Approval For Specifications Only.
  - \* This specification is subject to change without notice.

Please contact Powertip or it's representative before designing your product based on this specification.

Approval For Specifications and Sample.

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## **RECORDS OF REVISION**

Date	Rev.	Description	Note	Page
2005/12/11	0	PC2004LRU-CWA-HDHQ is the ROHS compliant part number based on Powertip's standard PC2004LRU-CWA-H-DH		

Total: 20 Page



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Note: For detailed information please refer to IC data sheet: <u>ST7066U, KS0063B</u>



### 1. SPECIFICATIONS

#### 1.1 Features

Item	Standard Value
Display Type	20*4 characters
LCD Type	STN, YG, Transflective, Positive, Extended Temp.
Driver Condition	LCD Module: 1/16 Duty, 1/5 Bias
Viewing Direction	6 O'clock
Backlight	Yellow-Green LED B/L
Weight	44g
Interface	_
Other(controller/driver IC)	_

1.2 Mechanical Specifications

Item	Standard Value	Unit		
Outline Dimension	77.0(L)* 47.0(W)*14.7 max.(H)	mm		
Viewing Area	60.0(L)* 22.0(W)	mm		
Active Area	55.12(L)* 17.56(W)	mm		
Dot Size	0.42(L)* 0.46(W)	mm		
Dot Pitch	0.47(L)* 0.51(W)	mm		

Note: For detailed information please refer to LCM drawing

1.3 Absolute Maximum Ratings

Item	Symbol	Condition	Min.	Max.	Unit
Power Supply Voltage	$V_{ m DD}$	_	-0.3	7.0	V
LCD Driver Supply Voltage	$V_{LCD}$	_	V <sub>DD</sub> -10.0	V <sub>DD</sub> +0.3	V
Input Voltage	V <sub>IN</sub>	_	-0.3	V <sub>DD</sub> +0.3	V
Operating Temperature	$T_{OP}$	Excluded B/L	-20	70	$^{\circ}\!\mathbb{C}$
Storage Temperature	$T_{ST}$	Excluded B/L	-30	80	$^{\circ}\!\mathbb{C}$
Storage Humidity	$H_{\mathrm{D}}$	Ta<40 °C	-	90	%RH



### 1.4 DC Electrical Characteristics

 $V_{DD}\!=5.0~V\pm10\%$  ,  $V_{SS}\!=0V$  ,  $Ta=25^{\circ}\!C$ 

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Logic Supply Voltage	$V_{ m DD}$	ĺ	4.5	5.0	5.5	V
"H" Input Voltage	$V_{IH}$	_	0.7VDD	-	$V_{DD}$	V
"L" Input Voltage	$V_{IL}$	ĺ	-0.3	1	0.6	V
"H" Output Voltage	$V_{\mathrm{OH}}$	IOH=-0.1mA	3.9	-	$V_{\mathrm{DD}}$	V
"L" Output Voltage	$V_{OL}$	IOL=0.1mA	-	-	0.4	V
Supply Current	$I_{DD}$	$V_{DD} = 5.0 \text{ V}$	-	3.4	5.0	mA
		0℃	-	-	-	
LCM Driver Voltage	$V_{\mathrm{OP}}$	25°C *1	5.4	5.6	5.8	V
		<b>5</b> 0℃	-	-	-	

# Note: \*1. The Vop test point is $V_{DD}$ - $V_{O}$ .

## 1.5 Optical Characteristics

LCD Panel : 1/16 Duty , 1/5 Bias ,  $V_{LCD} = 5.6 \ V$  ,  $Ta = 25 ^{\circ} C$ 

Item	Symbol	Conditions	Min.	Тур.	Max.	Reference
View Angle	θ	$C \ge 2.0, \varnothing = 0^{\circ}$	40°	-	-	Notes 1 & 2
Contrast Ratio	С	$\theta = 5^{\circ}, \varnothing = 0^{\circ}$	5	7	-	Note 3
Response Time(rise)	tr	$\theta = 5^{\circ}, \varnothing = 0^{\circ}$	-	200ms	300ms	Note 4
Response Time(fall)	tf	$\theta = 5^{\circ}, \varnothing = 0^{\circ}$	-	150ms	225ms	Note 4



#### Note 1: Definition of angles $\theta$ and $\emptyset$

Light (when reflected)  $z (\theta=0^{\circ})$ 

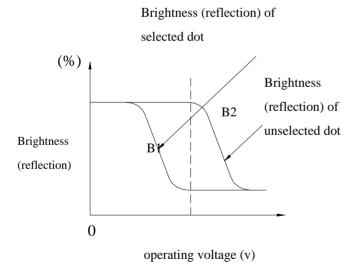
Light (when transmitted )  $Y(\varnothing=0^{\circ})$   $(\theta=90^{\circ})$ 

#### Note 3: Definition of contrast C

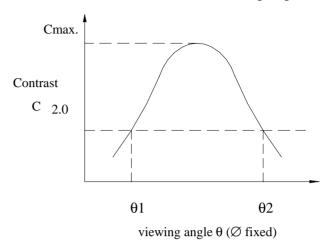
C = -

Brightness (reflection) of unselected dot (B2)

Brightness (reflection) of selected dot (B1)

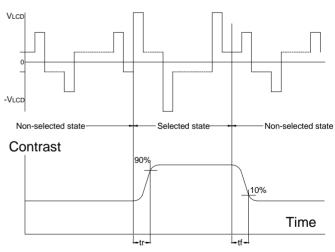


Note 2: Definition of viewing angles  $\theta 1$  and  $\theta 2$ 



Note: Optimum viewing angle with the naked eye and viewing angle  $\theta$  at Cmax. Above are not always the same

Note 4: Definition of response time



Note: Measured with a transmissive LCD panel which is displayed 1 cm<sup>2</sup>

$$\begin{split} V_{LCD} : Operating \ voltage \ f_{FRM} : Frame \ frequency \\ t_r \quad : Response \ time \ (rise) \quad t_f : Response \ time \ (fall) \end{split}$$



# 1.6 Backlight Characteristics

## LCD Module with LED Backlight

### Maximum Ratings

Item	Symbol	Conditions	Min.	Max.	Unit
Forward Current	IF	TA=25°C	-	375	mA
Reverse Voltage	VR	TA=25°C	-	8	V
Power Dissipation	PO	TA=25°C	-	1.72	W
Operating Temperature	$T_{OP}$	-	-20	70	$^{\circ}\!\mathbb{C}$
Storage Temperature	$T_{ST}$	-	-40	80	$^{\circ}\!\mathbb{C}$
Solder Temp. for 3 Second	-	-	-	260	$^{\circ}\!\mathbb{C}$

### Electrical / Optical Characteristics

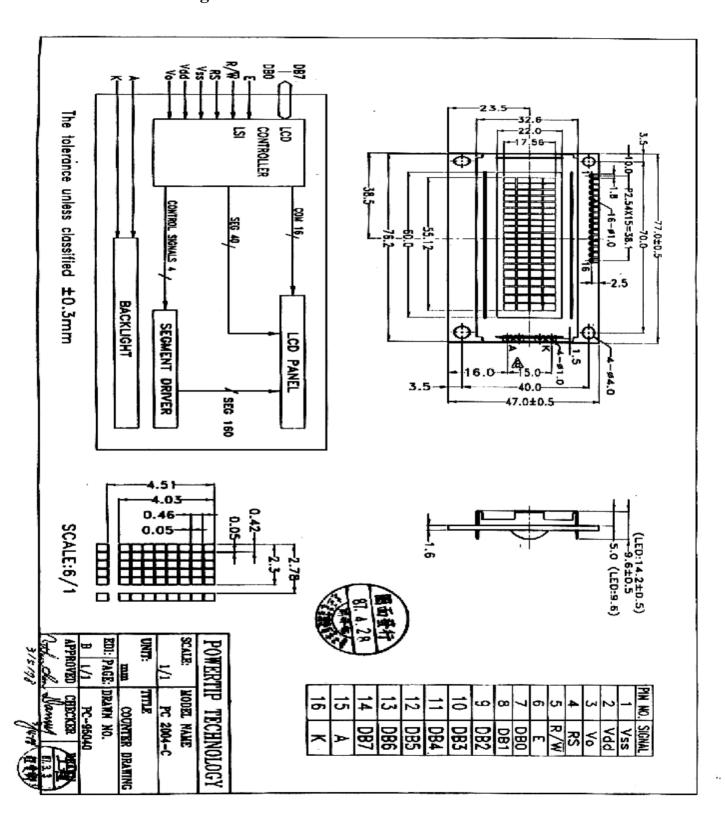
 $Ta = 25^{\circ}C$ 

Item	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward Voltage	VF	IF=150mA	3.8	4.2	4.6	V
Reverse Current	IR	VR= 8 V	-	-	0.2	mA
Average Brightness (with LCD)	IV	IF=150mA	-	-	-	cd/m <sup>2</sup>
Average Brightness (without LCD)	IV	IF=150mA	200	250	-	cu/m
Wavelength	λр	IF=150mA	571	_	576	nm
Color	Yellow-Green					



### 2. MODULE STRUCTURE

### 2.1 Counter Drawing

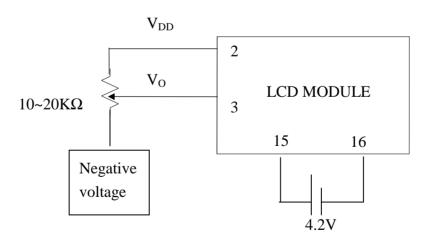




# 2.1 Interface Pin Description

Pin No.	Symbol	Signal Description				
1	Vss	Power Supply (Vss=0)				
2	Vdd	Power Supply (V <sub>DD</sub> >V <sub>SS</sub> )				
3	Vo	Operating voltage for LCD				
		Register Selection input				
4	RS	High = Data register				
4	KS	Low = Instruction register (for write)				
	Busy flag address counter (for read)					
5		Read/Write signal input is used to select the read/write mode				
3	R/W	High = Read mode, Low = Write mode				
6	Е	Start enable signal to read or write the data				
		Four low order bi-directional three-state data bus lines. Use for				
7~10	DB0 ~ DB3	data transfer between the MPU and the LCD module.				
		These four are not used during 4-bit operation.				
		Four high order bi-directional three-state data bus lines. Used				
11~14	DB4 ~ DB7	for data transfer between the MPU and the LCD module.				
		DB7 can be used as a busy flag.				
15	A	Power supply for LED B / L (+ )				
16	K	Power supply for LED B / L (- )				

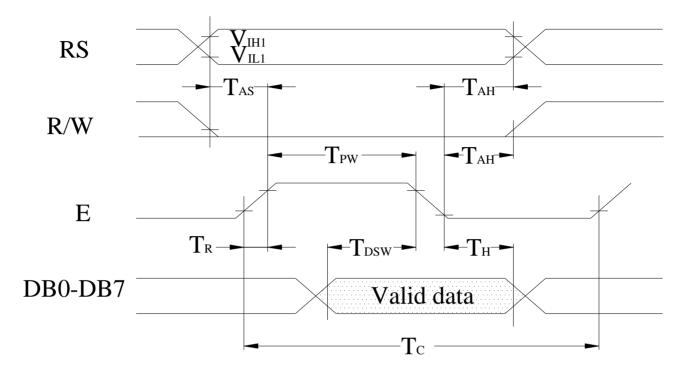
Contrast Adjust



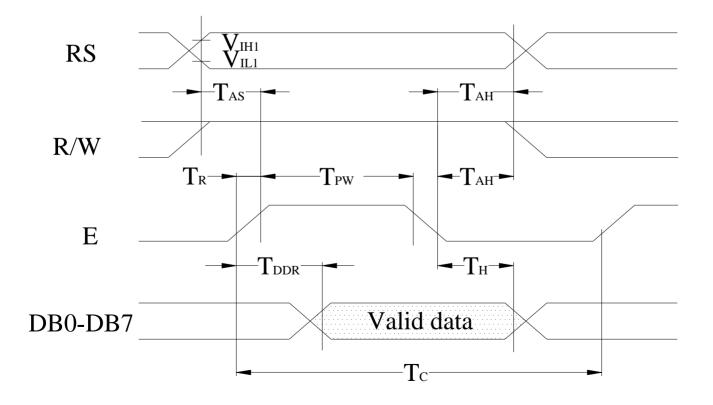


## 2.3 2.3 Timing Characteristics

Writing data from MPU to ST7066U



## I Reading data from ST7066U to MPU





## • Write Mode (Writing data from MPU to ST7066U)

 $(VDD = +5V + 10\%, Ta = 25^{\circ}C)$ 

				`		
Symbol	Characteristics	Test Condition	Min.	Тур.	Max.	Unit
$T_{\rm C}$	Enable Cycle Time	Pin E	1200	-	-	ns
$T_{PW}$	Enable Pulse Width	Pin E	140	-	-	ns
$T_R, T_F$	Enable Rise / Fall Time	Pin E	-	ı	25	ns
$T_{AS}$	Address Setup Time	Pins: RS , RW,E	0	-	-	ns
$T_{AH}$	Address Hold Time	Pins :RS,RW,E	10	-	-	ns
$T_{DSW}$	Data Setup Time	Pins:DB0~DB7	40	-	-	ns
$T_{H}$	Data Hold Time	Pins:DB0~DB7	10	-	-	ns

# • Read Mode (Reading data from ST7066U to MPU)

 $(VDD = +5V \pm 10\%, Ta = 25^{\circ}C)$ 

					,	
Symbol	Characteristics	Test Condition	Min.	Тур.	Max.	Unit
$T_{\rm C}$	Enable Cycle Time	Pin E	1200	-	-	ns
$T_{PW}$	Enable Pulse Width	Pin E 140		-	-	ns
$T_R, T_F$	Enable Rise / Fall Time	Pin E	-	-	25	ns
T <sub>AS</sub>	Address Setup Time	Pins: RS , RW,E	0	-	-	ns
$T_{AH}$	Address Hold Time	Pins :RS,RW,E	10	-	-	ns
$T_{\mathrm{DDR}}$	Data Setup Time	Pins:DB0~DB7	-	-	100	ns
$T_{H}$	Data Hold Time	Pins:DB0~DB7	10	-	-	ns



# 2.4 Display Command

	Instruction Code							Descriptio					
Instructions			DB	DB	DB	DB	DB	DB	DB	DB	Description	n Time	
	RS	R/W	7	6	5	4	3	2	1	0		(270KHz)	
											Write "20H" to DDRAM, and set	` ′	
Clear	0	0	0	0	0	0	0	0	0	1	DDRAM address to "00H" from	1.52ms	
Display											AC.		
											Set DDRAM address to "00H"		
Datum											from AC and return cursor to it's		
Return Home	0	0	0	0	0	0	0	0	1	×	original position if shifted.	1.52ms	
поше											The contents of DDRAM are not		
											changed.		
											Sets cursor move direction and		
Entry Mode	0	0	0	0	0	0	0	1	I/D	S	specifies display shift. These	37µs	
Set	O							1	1/10		operations are performed during	57μ5	
											data write and read.		
Display											D=1 : entire display on		
ON/OFF	0	0	0	0	0	0	1	D	С	В	C=1 : cursor on	37µs	
											B=1 : cursor position on		
Cursor or											Set cursor moving and display		
Display	0	0	0	0	0	1	S/C	R/L	×	×	shift control bit, and the direction,	37µs	
Shift											without changing of DDRAM	'	
											data.		
Function			•					_			DL: interface data is 8/4 bits		
Set	0	0	0	0	1	DL	N	F	×	×	NL: number of line is 2/1	37µs	
G . CCD ANA					4.0	1.0	10	4.0	4.0	4.0	F: font size is 5×11/5×8		
Set CGRAM	0	0	0	1	AC	AC	AC	AC	AC	AC	Set CGRAM address in address	37µs	
Address				4.0	5	4	3	2	1	0	counter.		
Set DDRAM	0	0	1	AC	AC	AC	AC	AC	AC	AC	Set DDRAM address in address	37µs	
Address				6	5	4	3	2	1	0	counter.		
Read Busy				٨	<b>A.</b> C	AC	AC	AC	AC	AC	Whether during internal operation		
Flag and	0	1	BF	AC 6	AC 5	AC 4	3	$\frac{AC}{2}$	AC 1	0	or not can be known by reading BF. The contents of address	0µs	
Address				U	3	4	3		1	U	counter can also be read.		
											counter can also be fead.		



Write Data	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write data into internal RAM	37us	
to RAM	1	U	יט	Do		DŦ	DS	D2	Di	Do	(DDRAM/CGRAM).	37μ8	
Read Data	1	1	D7	D6	D5	D4	D2	D2	D1		Read data from internal RAM	27	
from RAM	1	1	D/	טט	טט	D4	D3	D2	וטו	D0	(DDRAM/CGRAM).	37µs	

#### Note:

Be sure the ST7066U is not in the busy state (BF=0) before sending an instruction from the MPU to the ST7066. If an instruction is sent without checking the busy flag, the time between the first instruction and next instruction will take much longer than the instruction time itself.

Refer to Instruction Table for the list of each instruction execution time



### 2.5 Character Pattern

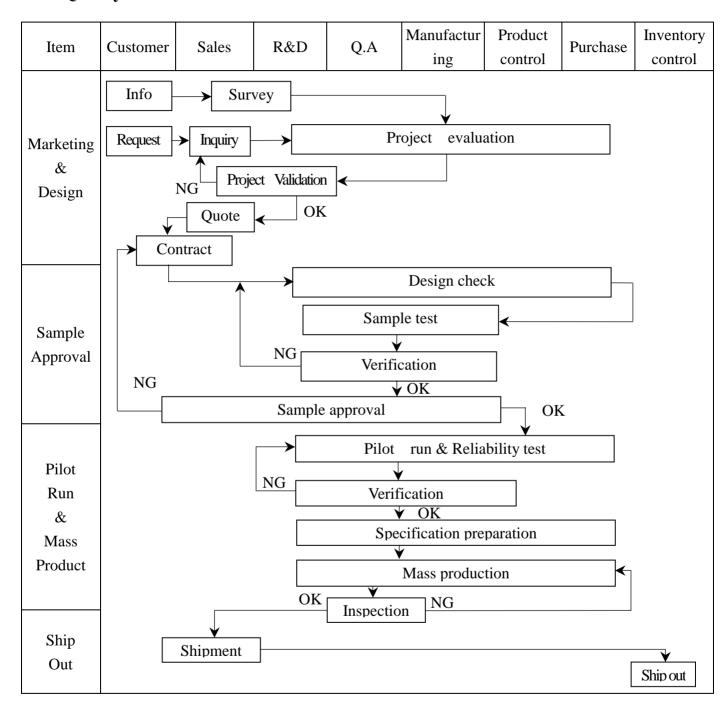
# ■ CHARACTER PATTERN(SO/HO/EA,WA)

Lower 4 Bits	0,000	0001	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	1100	1101	1110	1111
xxxx0000	CG RAM (1)					=====		<b></b> -					-===	===_		
xxxx0001	(2)		<b>!</b>	-1-	<b></b>		-===	-:::			Ħ	<u>_</u> ==		<u></u> :	-:::	
xxxx0010	(3)	_	==	:				 						_===		
xxxx0011	(4)			:		==	<b>=</b>					<b>!</b>				=2-Z=
xxxx0100	(5)	i	=		====			- <u>-</u>					<b>!</b>		<b></b> ;	
xxxx0101	(6)		 				====	<b>.</b>			:=				<b>=</b>	
xxxx0110	(7)					<b>I</b> !		==		<u>.</u>						
xxxx0111	(8)		]#					I_=_I					_===			H
xxxx1000	(1)		₹	==		==	<b>!</b> ;				<b>!</b> -	-:::		<b>!</b> .!	•, <b>!</b>	
xxxx1001	(2)					:!					=::	-":[	!	<b>i</b>	[	
xxxx1010	(3)		- <b></b> -:	##	•		:							·		::::
xxxx1011	(4)			::	<b>-:</b>		-:	=			_= <b>=</b> -		<b></b>		I <b>-</b> I	.= <u>:</u>
xxxx1100	(5)		:=	-:-	<b></b>					 				:I	-::::	
xxxx1101	(6)										=-			 		
xxxx1110	(7)		==			 :		:	i	 			===	•••		
xxxx1111	(8)		• • • • •					- <b>=</b> =			= <b>=</b> .=	==	:			

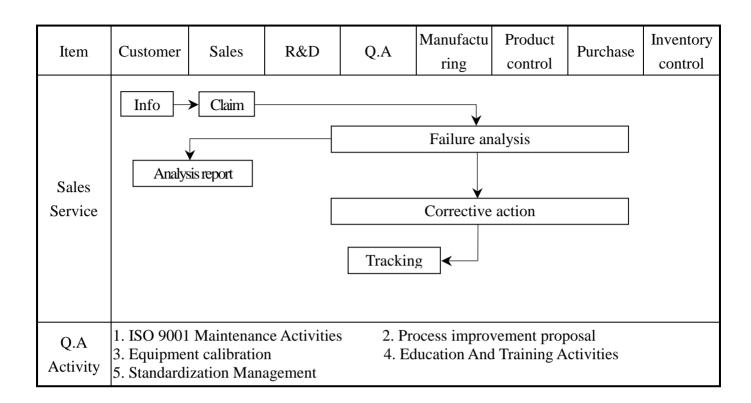


## 3. QUALITY ASSURANCE SYSTEM

### 3.1 Quality Assurance Flow Chart









### 3.2 Inspection Specification

Inspection Standard: MIL-STD-105E Table Normal Inspection Single Sampling Level II •

Equipment: Gauge · MIL-STD · Powertip Tester · Sample ·

IQC Defect Level: Major Defect AQL 0.4; Minor Defect AQL 1.5 °

FQC Defect Level: 100% Inspection • OUT Going Defect Level: Sampling •

Specification:

NO	Item	Specification	Judge	Level
1	Part Number	The part number is inconsistent with work order of production	N.G.	Major
2	Quantity	The quantity is inconsistent with work order of production	N.G.	Major
	Electronic	The display lacks of some patterns.	N.G.	Major
	characteristics of	Missing line.	N.G.	Major
3	LCM	The size of missing dot, A is $> 1/2$ Dot size	N.G.	Major
	$A=(L+W)\div 2$	There is no function.	N.G.	Major
		Output data is error	N.G.	Major
		Material is different with work order of production	N.G.	Major
		LCD is assembled in inverse direction	N.G.	Major
		Bezel is assembled in inverse direction	N.G.	Major
		Shadow is within LCD viewing area + 0.5 mm	N.G.	Major
	Appearance of	The diameter of dirty particle, A is > 0.4 mm		Minor
	LCD A=(L+W)÷2  Dirty particle (Including scratch \cdot bubble)	Dirty particle length is $> 3.0$ mm, and $0.01$ mm $<$ width $\le 0.05$ mm	N.G.	Minor
4		Display is without protective film	N.G.	Minor
		Conductive rubber is over bezel 1mm	N.G.	Minor
		Polarizer exceeds over viewing area of LCD	N.G.	Minor
		Area of bubble in polarizer, $A > 1.0$ mm, the number of bubble is $> 1$ piece.	N.G.	Minor
		0.4 mm < Area of bubble in polarizer, A < 1.0 mm, the number of bubble is $>4$ pieces.	N.G.	Minor
		Burned area or wrong part number is on PCB	N.G.	Major
		The symbol, character, and mark of PCB are unidentifiable.	N.G	Minor
		The stripped solder mask, A is > 1.0mm	N.G.	Minor
		0.3mm < stripped solder mask or visible circuit, A <	N. C	3.61
	Appearance of	1.0mm, and the number is $\geq 4$ pieces	N.G.	Minor
5	PCB	There is particle between the circuits in solder mask	N.G	Minor
	$A=(L+W)\div 2$	The circuit is peeled off or cracked	N.G	Minor
		There is any circuits risen or exposed.	N.G	Minor
		$0.2 \text{mm} < \text{Area of solder ball, A is } \leq 0.4 \text{mm}$ The number of solder ball is $\geq 3$ pieces	N.G	Minor
		The magnitude of solder ball, A is >0.4mm.	N.G	Minor



NO	Item	Specification	Judge	Level
		The shape of modeling is deformed by touching.	N.G.	Major
	Appearance of	Insufficient epoxy: Circuit or pad of IC is visible	N.G.	Minor
6	molding A=(L+W)÷2	Excessive epoxy: Diameter of modeling is >20mm or height is >2.5mm	N.G.	Minor
		The diameter of pinhole in modeling, A is $> 0.2$ mm.	N.G.	Minor
		The folding angle of frame must be $>45^{\circ} +10^{\circ}$	N.G.	Minor
7	Appearance of frame	The area of stripped electroplate in top-view of frame, A is > 1.0mm.	N.G.	Minor
'	$A=(L+W)\div 2$	Rust or crack is (Top view only)	N.G.	Minor
		The scratched width of frame is $> 0.06$ mm. (Top view only)	N.G.	Minor
	Electrical	The color of backlight is nonconforming	N.G.	Major
	Electrical characteristic of	Backlight can't work normally.	N.G.	Major
8	backlight	The LED lamp can't work normally	N.G.	Major
0	A=( L+W )÷2	The unsoldering area of pin for backlight, A is >1/2 solder joint area.	N.G.	Minor
	$A=(L+W)\cdot Z$	The height of solder pin for backlight is >2.0mm	N.G.	Minor
	Assembly parts	The mark or polarity of component is unidentifiable.	N.G.	Minor
		The height between bottom of component and surface of the PCB is floating >0.7mm		Minor
10		D> 1/4W  W D D D Pad	N.G.	Minor
	A=( L+W )÷2	End solder joint width, D' is >50% width of component termination or width of pad	N.G.	Minor
		Side overhang, D is >25% width of component termination.	N.G.	Minor
		Component is cracked, deformed, and burned, etc.	N.G.	Minor
		The polarity of component is placed in inverse direction.	N.G.	Minor
		Maximum fillet height of solder extends onto the component body or minimum fillet height is $< 0.5 \text{mm}$ .	N.G.	Minor



## 4. RELIABILITY TEST

# 4.1 Reliability Test Condition

NO	Item	Test Condition						
1	High Temperature Storage	Storage at 80 ±2°C 96~100 hrs Surrounding temperature, then storage at normal condition 4hrs						
2	Low Temperature Storage	Storage at -30 ±2°C 96~100 hrs Surrounding temperature, then storage at normal condition 4hrs						
3	High Temperature /Humidity Storage	1.Storage 96~100 hrs 60±2°C, 90~95%RH surrounding temperature, then storage at normal condition 4hrs. (Excluding the polarizer).  or  2.Storage 96~100 hrs 40±2°C, 90~95%RH surrounding temperature, then storage at normal condition 4 hrs.						
4	Temperature Cycling	$-20^{\circ}\text{C} \rightarrow 25^{\circ}\text{C} \rightarrow 70^{\circ}\text{C} \rightarrow 25^{\circ}\text{C}$ $(30\text{mins}) (5\text{mins}) (30\text{mins}) (5\text{mins})$ $10 \text{ Cycle}$						
5	Vibration	10~55Hz (1 minute) 1.5mm X,Y and Z direction * (each 2hrs)						
6	ESD Test	Air Discharge: Apply 6 KV with 5 times discharge for each polarity +/- Testing location: Around the face of LCD	Contact Discharge: Apply 250V with 5 times discharge for each polarity +/- Testing location: 1.Apply to bezel. 2.Apply to Vdd, Vss.					
7	Drop Test	Packing Weight (Kg)  0 ~ 45.4  45.4 ~ 90.8  90.8 ~ 454  Over 454	Drop Height (cm)  122  76  61  46					



#### 5. PRECAUTION RELATING PRODUCT HANDLING

#### **5.1 SAFETY**

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

#### **5.2 HANDLING**

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So, please handle it very carefully, do not touch, push or rub the exposed polarizing with anything harder than an HB pencil lead (glass, tweezers, etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is 320±10°C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM.

#### **5.3 STORAGE**

- 5.3.1 Store the panel or module in a dark place where the temperature is  $25^{\circ}$ C  $\pm 5^{\circ}$ C and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush, shake, or jolt the module.

#### **5.4 TERMS OF WARRANTY**

- 5.4.1 Applicable warrant period

  The period is within thirteen months since the date of shipping out under normal using and storage conditions.
- 5.4.2 Unaccepted responsibility
  - This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment , we cannot take responsibility if the product is used in nuclear power control equipment , aerospace equipment , fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.