

1.2 Initialization of Micro Display:

The Micro display should be initialized during POWER ON for proper functionality. This is carried out using the micro controller through SMBUS. The micro controller acts as MASTER and initializes the SLAVE micro display. The address of the SLAVE is 0x64. Though the micro display can be configured for color display, in the present configuration it is configured for Monochrome composite video input. There are totally ~~26~~²⁷ registers in the micro display and the values that are set in the present configuration are listed in the following Table:

ADDRESS	DATA	NAME
0x0000	0x01	STAT
0x0001	0x78	RGAIN
0x0002	0x38	ROFF
0x0003	0x78	GGAIN
0x0004	0x38	GOFF
0x0005	0x78	BGAIN
0x0006	0x38	BOFF
0x0007	0x81	MGAIN
0x0008	0x39	MOFF
0x0009	0x4A	VMODE
0x000A	0x10	HMODE
0x000B	0x00	BR_L
0x000C	0x02	BR_U
0x000D	0x00	HRATE_L
0x000E	0x80	HRATE_U
0x000F	0xC0	PLL_L
0x0010	0x1B	PLL_U
0x0011	0x07	PIF
0x0012	0x00	PI2
0x0013	0x90	HSTART
0x0014	0x0F	VSTART
0x0015	0xD8	HBLK
0x0016	0x0C	HDEL
0x0017	0x01	PDWN
0x0018	0x80	ATB
0x0019	0x00	AMTEST
0x001A	0xC0	TRIM